

MCU - GPIOA/B/C

U7B

	WK_UP	PA0	N5	PA0/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/TIM15_BKIN/USART2_CTS_NSS/UART4_TX/SDMMC2_CMD/SAI2_SD_B/ETH_MII_CRS/ADC_INP16/WKUP0
		PA0_C	T1	PA0_C/ADC12_INN1_INP0
	RMII_REF_CLK	PA1	N4	PA1/TIM2_CH2/TIM5_CH2/LPTIM3_OUT/TIM15_CH1N/USART2_RTS/UART4_RX/QUADSPI_BK1_IO3/SAI2_MCK_B/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/LCD_R2/ADC1_INN16_INP17
		PA1_C	T2	PA1_C/ADC12_INP1
USART2_TX	ETH_MDIO	PA2	N3	PA2/TIM2_CH3/TIM5_CH3/LPTIM4_OUT/TIM15_CH1/USART2_TX/SAI2_SCK_B/ETH_MDIO/MDIOS_MDIO/LCD_R1/ADC12_INP14/WKUP1
USART2_RX	PWM_DAC	PA3	U2	PA3/TIM2_CH4/TIM5_CH4/LPTIM5_OUT/TIM15_CH2/USART2_RX/LCD_B2/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC12_INP15
DCMI_HREF	STM_DAC	PA4	U3	PA4/TIM5_ETR/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/USART2_CK/SPI6_NSS/OTG_HS_SOF/DCMI_HSYNC/LCD_VSYNC/ADC12_INP18/DAC1_OUT1
	STM_ADC	PA5	T3	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/I2S1_CK/SPI6_SCK/OTG_HS_ULPI_CK/LCD_R4/ADC12_INN18_INP19/DAC1_OUT2
DCMI_PCLK		PA6	R3	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SPI1_MISO/I2S1_SDI/SPI6_MISO/TIM13_CH1/TIM8_BKIN_COMP12/MDIOS_MDC/TIM1_BKIN_COMP12/DCMI_PIXCLK/LCD_G2/ADC12_INP3
	RMII_CS_DV	PA7	R5	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/I2S1_SDO/SPI6_MOSI/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_SDNEW/ADC12_INN3_INP7/OPAMP1_VINM
DCMI_XCLK	REMOTE_IN	PA8	E15	PA8/MCO1/TIM1_CH1/HRTIM_CHB2/TIM8_BKIN2/I2C3_SCL/USART1_CK/OTG_FS_SOF/UART7_RX/TIM8_BKIN2_COMP12/LCD_B3/LCD_R6
	USART1_TX	PA9	D15	PA9/TIM1_CH2/HRTIM_CHC1/LPUART1_TX/I2C3_SMBA/SPI2_SCK/I2C2_CK/USART1_TX/FDCAN1_RXFD_MODE/ETH_TX_ER/DCMI_D0/LCD_R5/OTG_FS_VBUS
	USART1_RX	PA10	D14	PA10/TIM1_CH3/HRTIM_CHC2/LPUART1_RX/USART1_RX/FDCAN1_TXFD_MODE/OTG_FS_ID/MDIOS_MDIO/LCD_B4/DCMI_D1/LCD_B1
	USB_D-	PA11	E17	PA11/TIM1_CH4/HRTIM_CHD1/LPUART1_CTS/SPI2_NSS/I2S2_WS/UART4_RX/USART1_CTS_NSS/FDCAN1_RX/OTG_FS_DM/LCD_R4
	USB_D+	PA12	E16	PA12/TIM1_ETR/HRTIM_CHD2/LPUART1_RTS/SPI2_SCK/I2S2_CK/UART4_TX/USART1_RTS/SAI2_FS_B/FDCAN1_TX/OTG_FS_DP/LCD_R5
	JTMS	PA13	C15	PA13/JTMS_SWDIO
	JTCK	PA14	B14	PA14/JTCK-SWCLK
NRF_CS	JTDI	PA15	A14	PA15/JTDI/TIM2_CH1/TIM2_ETR/HRTIM_FLT1/HDMI_CEC/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/SPI6_NSS/UART4_RTS/UART7_TX
	LED1	PB0	U5	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/DFSDM_CKOUT/UART4_CTS/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/LCD_G1/ADC12_INN5_INP9/OPAMP1_VINP/COMP_1_INP
	LED0	PB1	T5	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/DFSDM_DATIN1/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/LCD_G0/ADC12_INP5/COMP_1_INM
	QSPI_BK1_CLK	PB2	R6	PB2/SAI1_D1/DFSDM_CKIN1/SAI1_SD_A/SPI3_MOSI/I2S3_SDO/SAI4_SD_A/QUADSPI_CLK/SAI4_D1/ETH_TX_ER/COMP_1_INP/RTC_OUT
DCMI_SDA	JTDO	PB3	C6	PB3/JTDO/TIM2_CH2/HRTIM_FLT4/SPI1_SCK/I2S1_CK/SPI3_SCK/I2S3_CK/SPI6_SCK/SDMMC2_D2/UART7_RX
DCMI_SCL	JTRST	PB4	B7	PB4/NTRST/TIM16_BKIN/TIM3_CH1/HRTIM_EEV6/SPI1_MISO/I2S1_SDI/SPI3_MISO/I2S3_SDI/SPI2_NSS/I2S2_WS/SPI6_MISO/SDMMC2_D3/UART7_TX
	LCD_BL	PB5	A5	PB5/TIM17_BKIN/TIM3_CH2/HRTIM_EEV7/I2C1_SMBA/SPI1_MOSI/I2S1_SDO/I2C4_SMBA/SPI3_MOSI/I2S3_SDO/SPI6_MOSI/FDCAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10/UART5_RX
	QSPI_BK1_NCS	PB6	B5	PB6/TIM16_CH1N/TIM4_CH1/HRTIM_EEV8/I2C1_SCL/HDMI_CEC/I2C4_SCL/USART1_TX/LPUART1_TX/FDCAN2_TX/QUADSPI_BK1_NCS/DFSDM_DATIN5/FMC_SDNE1/DCMI_D5/UART5_TX
DCMI_VSYNC		PB7	C5	PB7/TIM17_CH1N/TIM4_CH2/HRTIM_EEV9/I2C1_SDA/I2C4_SDA/USART1_RX/LPUART1_RX/FDCAN2_TXFD_MODE/DFSDM_CKIN5/FMC_NL/DCMI_VSYNC/PVD_IN
DCMI_D6		PB8	D5	PB8/TIM16_CH1/TIM4_CH3/DFSDM_CKIN7/I2C1_SCL/I2C4_SCL/SDMMC1_CKIN/UART4_RX/FDCAN1_RX/SDMMC2_D4/ETH_MII_TXD3/SDMMC1_D4/DCMI_D6/LCD_B6
DCMI_D7		PB9	D4	PB9/TIM17_CH1/TIM4_CH4/DFSDM_DATIN7/I2C1_SDA/SPI2_NSS/I2S2_WS/I2C4_SDA/SDMMC1_CDIR/UART4_TX/FDCAN1_TX/SDMMC2_D5/I2C4_SMBA/SDMMC1_D5/DCMI_D7/LCD_B7
	USART3_TX	PB10	P11	PB10/TIM2_CH3/HRTIM_SCOUT/LPTIM2_IN1/I2C2_SCL/SPI2_SCK/I2S2_CK/DFSDM_DATIN7/USART3_TX/QUADSPI_BK1_NCS/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
	USART3_RX	PB11	P12	PB11/TIM2_CH4/HRTIM_SCIN/LPTIM2_ETR/I2C2_SDA/DFSDM_CKIN7/USART3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
NRF_IRQ	GBC_LED	PB12	T14	PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/DFSDM_DATIN1/USART3_CK/FDCAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID/TIM1_BKIN_COMP12/UART5_RX
	SPI2_SCK	PB13	U14	PB13/TIM1_CH1N/LPTIM2_OUT/SPI2_SCK/I2S2_CK/DFSDM_CKIN1/USART3_CTS_NSS/FDCAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_TXD1/UART5_TX/OTG_HS_VBUS
	SPI2_MISO	PB14	U15	PB14/TIM1_CH2N/TIM12_CH1/TIM8_CH2N/USART1_TX/SPI2_MISO/I2S2_SDI/DFSDM_DATIN2/USART3_RTS/UART4_RTS/SDMMC2_D0/OTRG_HS_DM
	SPI2_MOSI	PB15	T15	PB15/RTC_REFIN/TIM1_CH3N/TIM12_CH2/TIM8_CH3N/USART1_RX/SPI2_MOSI/I2S2_SDO/DFSDM_CKIN2/UART4_CTS/SDMMC2_D1/OTG_HS_DP
	FMC_SDNWE	PC0	L2	PC0/DFSDM_CKIN0/DFSDM_DATIN4/SAI2_FS_B/OTG_HS_ULPI_STP/FMC_SDNEW/LCD_R5/ADC123_INP10
	ETH_MDC	PC1	M2	PC1/SAI1_D1/DFSDM_DATIN0/DFSDM_CKIN4/SPI2_MOSI/I2S2_SDO/SAI1_SD_A/SAI4_SD_A/SDMMC2_CK/SAI4_D1/ETH_MDC/MDIOS_MDC/ADC123_INN10_INP11/WKUP5
	FMC_SDNE0	PC2	M3	PC2/DFSDM_CKIN1/SPI2_MISO/I2S2_SDI/DFSDM_CKOUT/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_INN11_INP12
		PC2_C	R1	PC2_C/ADC3_INN1_INP0
	FMC_SDCKE0	PC3	M4	PC3/DFSDM_DATIN1/SPI2_MOSI/I2S2_SDO/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC12_INN12_INP13
		PC3_C	R2	PC3_C/ADC3_INP1
	RMII_RXD0	PC4	T4	PC4/DFSDM_CKIN2/I2S1_MCK/SPDIFRX_IN2/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_SDNE0/ADC12_INP4/OPAMP1_VOUT/COMP_1_INM
	RMII_RXD1	PC5	U4	PC5/SAI1_D3/DFSDM_DATIN2/SPDIFRX_IN3/SAI4_D3/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_SDCKE0/COMP_1_OUT/ADC12_INN4_INP8/OPAMP1_VINM
	DCMI_D0	PC6	F14	PC6/HRTIM_CHA1/TIM3_CH1/TIM8_CH1/DFSDM_CKIN3/I2S2_MCK/USART6_TX/SDMMC1_D0DIR/FMC_NWAIT/SDMMC2_D6/SDMMC1_D6/DCMI_D0/LCD_HSYNC/SWPMI_IO
	DCMI_D1	PC7	F13	PC7/TRGIO/HRTIM_CHA2/TIM3_CH2/TIM8_CH2/DFSDM_DATIN3/I2S3_MCK/USART6_RX/SDMMC1_D123DIR/FMC_NE1/SDMMC2_D7/SWPMI_TX/SDMMC1_D7/DCMI_D1/LCD_G6
SD1_D0	DCMI_D2	PC8	E13	PC8/HRTIM_CHB1/TIM3_CH3/TIM8_CH3/USART6_CK/UART5_RTS/FMC_NE2/FMC_NCE/SWPMI_RX/SDMMC1_D0/DCMI_D2
SD1_D1	DCMI_D3	PC9	E14	PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_IO0/LCD_G3/SWPMI_SUSPEND/SDMMC1_D1/DCMI_D3/LCD_B2
SD1_D2		PC10	A13	PC10/HRTIM_EEV1/DFSDM_CKIN5/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/QUADSPI_BK1_IO1/SDMMC1_D2/DCMI_D8/LCD_R2
SD1_D3	DCMI_D4	PC11	B13	PC11/HRTIM_FLT2/DFSDM_DATIN5/SPI3_MISO/I2S3_SDI/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SDMMC1_D3/DCMI_D4
SD1_CLK		PC12	C12	PC12/HRTIM_EEV2/SPI3_MOSI/I2S3_SDO/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9
	KEY2	PC13	E3	PC13/RTC_TAMP_1/RTC_TS/WKUP2

STM32H750XBH6

Title: ATK_STM32H750_CORE_BOARD	
Author: 正点原子团队	Size: A4
Date: 2023/12/21	File: STM32H750XB_PIN_ABC.SchDoc
Revision: *	Version: V1.2



MCU - GPIOD/E/F

FMC_D2	PD0	D13	U7C
FMC_D3	PD1	E12	PD0/DFSDM_CKIN6/SAI3_SCK_A/UART4_RX/FDCAN1_RX/FMC_D2/FMC_DA2
SD1_CMD	PD2	D12	PD1/DFSDM_DATIN6/SAI3_SD_A/UART4_TX/FDCAN1_TX/FMC_D3/FMC_DA3
DCMI_D5	PD3	B12	PD2/TIM3_ETR/UART5_RX/SDMMC1_CMD/DCMI_D11
FMC_NOE	PD4	A12	PD3/DFSDM_CKOUT/SP12_SCK/I2S2_CK/USART2_CTS_NSS/FMC_CLK/DCMI_D5/LCD_G7
FMC_NWE	PD5	A11	PD4/HRTIM_FLT3/SAI3_FS_A/USART2_RTS/FDCAN1_RXFD_MODE/FMC_NOE
FMC_NWAIT	PD6	B11	PD5/HRTIM_EEV3/USART2_TX/FDCAN1_TXFD_MODE/FMC_NWE
FMC_NE1	PD7	C11	PD6/SAI1_D1/DFSDM_CKIN4_DATIN1/SP13_MOSI/I2S3_SDO/SAI1_SD_A/USART2_RX/SAI4_SD_A/FDCAN2_RXFD_MODE/SAI4_D1/SDMMC2_CK/FMC_NWAIT/DCMI_D10/LCD_B2
FMC_D13	PD8	U16	PD7/DFSDM_DATIN4/SP11_MOSI/I2S1_SDO/DFSDM_CKIN1/USART2_CK/SPDIFRX_IN0/SDMMC2_CMD/FMC_NE1
FMC_D14	PD9	T17	PD8/DFSDM_CKIN3/SAI3_SCK_B/USART3_TX/SPDIFRX_IN1/FMC_D13/FMC_DA13
FMC_D15	PD10	T16	PD9/DFSDM_DATIN3/SAI3_SD_B/USART3_RX/FDCAN2_RXFD_MODE/FMC_D14/FMC_DA14
FMC_A16_CLE	PD11	R15	PD10/DFSDM_CKOUT/SAI3_FS_B/USART3_CK/FDCAN2_TXFD_MODE/FMC_D15/FMC_DA15/LCD_B3
FMC_A17_ALE	PD12	R16	PD11/LPTIM2_IN2/I2C4_SMBA/USART3_CTS_NSS/QUADSPI_BK1_IO0/SAI2_SD_A/FMC_A16
FMC_A18	PD13	R17	PD12/LPTIM1_IN1/TIM4_CH1/LPTIM2_IN1/I2C4_SCL/USART3_RTS/QUADSPI_BK1_IO1/SAI2_FS_A/FMC_A17
FMC_D0	PD14	P16	PD13/LPTIM1_OUT/TIM4_CH2/I2C4_SDA/QUADSPI_BK1_IO3/SAI2_SCK_A/FMC_A18
FMC_D1	PD15	P15	PD14/TIM4_CH3/SAI3_MCLK_B/UART8_CTS/FMC_D0/FMC_DA0
			PD15/TIM4_CH4/SAI3_MCLK_A/UART8_RTS/FMC_D1/FMC_DA1
FMC_NBL0	PE0	C4	
FMC_NBL1	PE1	B4	PE0/LPTIM1_ETR/TIM4_ETR/HRTIM_SCIN/LPTIM2_ETR/UART8_RX/FDCAN1_RXFD_MODE/SAI2_MCK_A/MFC_NBL0/DCMI_D2
SAI1_MCLKA	PE2	C3	PE1/LPTIM1_IN2/HRTIM_SCOUT/UART8_TX/FDCAN1_TXFD_MODE/FMC_NBL1/DCMI_D3
SAI1_SDB	PE3	D3	PE2/SAI1_CK1/SP14_SCK/SAI1_MCLK_A/SAI4_MCLK_A/QUADSPI_BK1_IO2/SAI4_CK1/ETH_MII_TXD3/FMC_A23
SAI1_FSA	PE4	D2	PE3/TIM15_BKIN/SAI1_SD_B/SAI4_SD_B/FMC_A19
SAI1_SCKA	PE5	D1	PE4/SAI1_D2/DFSDM_DATIN3/TIM15_CH1N/SP14_NSS/SAI1_FS_A/SAI4_FS_A/SAI4_D2/FMC_A20/DCMI_D4/LCD_B0
SAI1_SDA	PE6	E5	PE5/SAI1_CK2/DFSDM_CKIN3/TIM15_CH1/SP14_MISO/SAI1_SCK_A/SAI4_SCK_A/SAI4_CK2/FMC_A21/DCMI_D6/LCD_G0
FMC_D4	PE7	U9	PE6/TIM1_BKIN2/SAI1_D1/TIM15_CH2/SP14_MOSI/SAI1_SD_A/SAI4_SD_A/SAI4_D1/SAI2_MCK_B/TIM1_BKIN2_COMP12/FMC_A22/DCMI_D7/LCD_G1
FMC_D5	PE8	T9	PE7/TIM1_ETR/DFSDM_DATIN2/UART7_RX/QUADSPI_BK2_IO0/FMC_D4/FMC_DA4/OPAMP2_VOUT/COMP_2_INM
FMC_D6	PE9	P9	PE8/TIM1_CH1N/DFSDM_CKIN2/UART7_TX/QUADSPI_BK2_IO1/FMC_D5/FMC_DA5/COMP_2_OUT/OPAMP2_VINM
FMC_D7	PE10	N9	PE9/TIM1_CH1/DFSDM_CKOUT/UART7_RTS/QUADSPI_BK2_IO2/FMC_D6/FMC_DA6/OPAMP2_VINP/COMP_2_INP
FMC_D8	PE11	P10	PE10/TIM1_CH2N/DFSDM_DATIN4/UART7_CTS/QUADSPI_BK2_IO3/FMC_D7/FMC_DA7/COMP_2_INM
FMC_D9	PE12	R10	PE11/TIM1_CH2/DFSDM_CKIN4/SP14_NSS/SAI2_SD_B/FMC_D8/FMC_DA8/LCD_G3/COMP_2_INP
FMC_D10	PE13	T10	PE12/TIM1_CH3N/DFSDM_DATIN5/SP14_SCK/SAI2_SCK_B/FMC_D9/FMC_DA9/COMP_1_OUT/LCD_B4
FMC_D11	PE14	U10	PE13/TIM1_CH3/DFSDM_CKIN5/SP14_MISO/SAI2_FS_B/FMC_D10/FMC_DA10/COMP_2_OUT/LCD_DE
FMC_D12	PE15	R11	PE14/TIM1_CH4/SP14_MOSI/SAI2_MCK_B/FMC_D11/FMC_DA11/LCD_CLK
			PE15/TIM1_BKIN/HDMI_CEC/FMC_D12/FMC_DA12/TIM1_BKIN_COMP12/LCD_R7
FMC_A0	PF0	G4	PF0/I2C2_SDA/FMC_A0
FMC_A1	PF1	G3	PF1/I2C2_SCL/FMC_A1
FMC_A2	PF2	G1	PF2/I2C2_SMBA/FMC_A2
FMC_A3	PF3	H4	PF3/FMC_A3/ADC3_INP5
FMC_A4	PF4	J5	PF4/FMC_A4/ADC3_INN5_INP9
FMC_A5	PF5	J4	PF5/FMC_A5/ADC3_INP4
QSPI_BK1_IO3	PF6	K2	PF6/TIM16_CH1/SP15_NSS/SAI1_SD_B/UART7_RX/SAI4_SD_B/QUADSPI_BK1_IO03/ADC3_INN4_INP8
QSPI_BK1_IO2	PF7	K3	PF7/TIM17_CH1/SP15_SCK/SAI1_MCLK_B/UART7_TX/SAI4_MCLK_B/QUADSPI_BK1_IO2/ADC3_INP3
QSPI_BK1_IO0	PF8	K4	PF8/TIM16_CH1N/SP15_MISO/SAI1_SCK_B/UART7_RTS/SAI4_SCK_B/TIM13_CH1/QUADSPI_BK1_IO00/ADC3_INN3_INP7
QSPI_BK1_IO1	PF9	L4	PF9/TIM17_CH1N/SP15_MOSI/SAI1_FS_B/UART7_CTS/SAI4_FS_B/TIM14_CH1/QUADSPI_BK1_IO01/ADC3_INP2
F_CS	PF10	L3	PF10/TIM16_BKIN/SAI1_D3/QUADSPI_CLK/SAI4_D3/DCMI_D11/ADC3_INN2_INP6
FMC_SDNRAS	PF11	T7	PF11/SP15_MOSI/SAI2_SD_B/FMC_SDNRAS/DCMI_D12/ADC1_INP2
FMC_A6	PF12	R7	PF12/FMC_A6/ADC1_INN2_INP6
FMC_A7	PF13	P7	PF13/DFSDM_DATIN6/I2C4_SMBA/FMC_A7/ADC2_INP2
FMC_A8	PF14	P8	PF14/DFSDM_CKIN6/I2C4_SCL/FMC_A8/ADC2_INN2_INP6
FMC_A9	PF15	R9	PF15/I2C4_SDA/FMC_A9

STM32H750XBH6

Title: ATK_STM32H750_CORE_BOARD	
Author: 正点原子团队	Size: A4
Date: 2023/12/21	File: STM32H750XB_PIN_DEF.SchDoc
Revision: *	Version: V1.2



MCU - GPIOG/H/I/J/K

U7D

FMC_A10	PG0	T8
FMC_A11	PG1	U8
FMC_A12	PG2	H16
T_MISO	PG3	H15
FMC_BA0	PG4	H14
FMC_BA1	PG5	G14
NRF_CE	PG6	G15
T_MOSI	PG7	F16
FMC_SDCLK	PG8	F15
FMC_NCE3	PG9	A10
I2C_INT	PG10	A9
RMII_TX_EN	PG11	B9
SPDIF_RX	DCMI_RESET	PG12 C9
RMII_TXD0	PG13	D9
RMII_TXD1	PG14	D8
FMC_SDNCAS	PG15	D6
KEY1	PH2	N2
KEY0	PH3	P2
IIC_SCL	PH4	P3
IIC_SDA	PH5	P4
T_SCK	PH6	T11
T_PEN	PH7	U13
FMC_D16	PH8	T13
FMC_D17	PH9	R13
FMC_D18	PH10	P13
FMC_D19	PH11	P14
FMC_D20	PH12	R14
FMC_D21	PH13	D16
FMC_D22	PH14	B17
FMC_D23	PH15	B16
FMC_D24	PI0	A16
FMC_D25	PI1	A15
FMC_D26	PI2	B15
FMC_D27	PI3	C14
FMC_NBL2	PI4	A4
FMC_NBL3	PI5	A3
FMC_D28	PI6	A2
FMC_D29	PI7	B3
T_CS	PI8	E4
FMC_D30	PI9	E2
FMC_D31	PI10	F3
GBC_KEY	PI11	F4
LCD_HSYNC	PI12	H1
LCD_VSYNC	PI13	H2
LCD_CLK	PI14	H3
LCD_R0	PI15	P5

PG0/FMC_A10
PG1/FMC_A11/OPAMP2_VINM
PG2/TIM8_BKIN/TIM8_BKIN_COMP12/FMC_A12
PG3/TIM8_BKIN2/TIM8_BKIN2_COMP12/FMC_A13
PG4/TIM1_BKIN2/TIM1_BKIN2_COMP12/FMC_A14/FMC_BA0
PG5/TIM1_ETR/FMC_A15/FMC_BA1
PG6/TIM17_BKIN/HRTIM_CHE1/QUADSPI_BK1_NCS/FMC_NE3/DCMI_D12/LCD_R7
PG7/HRTIM_CHE2/SAI1_MCLK_A/USART6_CK/FMC_INT/DCMI_D13/LCD_CLK
PG8/TIM8_ETR/SPI6_NSS/USART6_RTS/SPDIFRX_IN2/ETH_PPS_OUT/FMC_SDCLK/LCD_G7
PG9/SPI1_MISO/I2S1_SD/USART6_RX/SPDIFRX_IN3/QUADSPI_BK2_IO2/SAI2_FS_B/FMC_NE2/FMC_NCE/DCMI_VSYNC
PG10/HRTIM_FLT5/SPI1_NSS/I2S1_WS/LCD_G3/SAI2_SD_B/FMC_NE3/DCMI_D2/LCD_B2
PG11/HRTIM_EEV4/SPI1_SCK/I2S1_CK/SPDIFRX_IN0/SDMMC2_D2/ETH_MII_TX_EN/ETH_RMII_TX_EN/DCMI_D3/LCD_B3
PG12/LPTIM1_IN1/HRTIM_EEV5/SPI6_MISO/USART6_RTS/SPDIFRX_IN1/LCD_B4/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_NE4/LCD_B1
PG13/LPTIM1_OUT/HRTIM_EEV10/SPI6_SCK/USART6_CTS_NSS/ETH_MII_TXD0/ETH_RMII_TXD0/FMC_A24/LCD_R0
PG14/LPTIM1_ETR/SPI6_MOSI/USART6_TX/QUADSPI_BK2_IO3/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_A25/LCD_B0
PG15/USART6_CTS_NSS/FMC_SDNCAS/DCMI_D13
PH2/LPTIM1_IN2/QUADSPI_BK2_IO0/SAI2_SCK_B/ETH_MII_CRS/FMC_SDCKE0/LCD_R0/ADC3_INP13
PH3/QUADSPI_BK2_IO1/SAI2_MCK_B/ETH_MII_COL/FMC_SDNE0/LCD_R1/ADC3_INN13_INP14
PH4/I2C2_SCL/LCD_G5/OTG_HS_ULPI_NXT/LCD_G4/ADC3_INN14_INP15
PH5/I2C2_SDA/SPI5_NSS/FMC_SDNEW/ADC3_INN15_INP16
PH6/TIM12_CH1/I2C2_SMBA/SPI5_SCK/ETH_MII_RXD2/FMC_SDNE1/DCMI_D8
PH7/I2C3_SCL/SPI5_MISO/ETH_MII_RXD3/FMC_SDCKE1/DCMI_D9
PH8/TIM5_ETR/I2C3_SDA/FMC_D16/DCMI_HSYNC/LCD_R2
PH9/TIM12_CH2/I2C3_SMBA/FMC_D17/DCMI_D0/LCD_R3
PH10/TIM5_CH1/I2C4_SMBA/FMC_D18/DCMI_D1/LCD_R4
PH11/TIM5_CH2/I2C4_SCL/FMC_D19/DCMI_D2/LCD_R5
PH12/TIM5_CH3/I2C4_SDA/FMC_D20/DCMI_D3/LCD_R6
PH13/TIM8_CH1N/UART4_TX/FDCAN1_TX/FMC_D21/LCD_G2
PH14/TIM8_CH2N/UART4_RX/FDCAN1_RX/FMC_D22/DCMI_D4/LCD_G3
PH15/TIM8_CH3N/FDCAN1_TXFD_MODE/FMC_D23/DCMI_D11/LCD_G4
PI0/TIM5_CH4/SPI2_NSS/I2S2_WS/FDCAN1_RXFD_MODE/FMC_D24/DCMI_D13/LCD_G5
PI1/TIM8_BKIN2/SPI2_SCK/I2S2_CK/TIM8_BKIN2_COMP12/FMC_D25/DCMI_D8/LCD_G6
PI2/TIM8_CH4/SPI2_MISO/I2S2_SDI/FMC_D26/DCMI_D9/LCD_G7
PI3/TIM8_ETR/SPI2_MOSI/I2S2_SDO/FMC_D27/DCMI_D10
PI4/TIM8_BKIN/SAI2_MCK_A/TIM8_BKIN_COMP12/FMC_NBL2/DCMI_D5/LCD_B4
PI5/TIM8_CH1/SAI2_MCK_A/FMC_NBL3/DCMI_VSYNC/LCD_B5
PI6/TIM8_CH2/SAI2_SD_A/FMC_D28/DCMI_D6/LCD_B6
PI7/TIM8_CH3/SAI2_FS_A/FMC_D29/DCMI_D7/LCD_B7
PI8/RTC_TAMP_2/RTC_TS/WKUP3
PI9/UART4_RX/FDCAN1_RX/FMC_D30/LCD_VSYNC
PI10/FDCAN1_RXFD_MODE/ETH_MII_RX_ER/FMC_D31/LCD_HSYNC
PI11/LCD_G6/OTG_HS_ULPI_DIR/WKUP4
PI12/ETH_TX_ER/LCD_HSYNC
PI13/LCD_VSYNC
PI14/LCD_CLK
PI15/LCD_G2/LCD_R0
PJ0/LCD_R7/LCD_R1
PJ1/LCD_R2
PJ2/LCD_R3
PJ3/LCD_R4
PJ4/LCD_R5
PJ5/LCD_R6
PJ6/TIM8_CH2/LCD_R7
PJ7/TRGIN/TIM8_CH2N/LCD_G0
PJ8/TIM1_CH3N/TIM8_CH1/UART8_TX/LCD_G1
PJ9/TIM1_CH3/TIM8_CH1N/UART8_RX/LCD_G2
PJ10/TIM1_CH2N/TIM8_CH2/SPI5_MOSI/LCD_G3
PJ11/TIM1_CH2/TIM8_CH2N/SPI5_MISO/LCD_G4
PJ12/TRGOU/LCD_G3/LCD_B0
PJ13/LCD_B4/LCD_B1
PJ14/LCD_B2
PJ15/LCD_B3
PK0/TIM1_CH1N/TIM8_CH3/SPI5_SCK/LCD_G5
PK1/TIM1_CH1/TIM8_CH3N/SPI5_NSS/LCD_G6
PK2/TIM1_TIM8_BKIN/TIM8_TIM1_BKIN_COMP12/LCD_G7
PK3/LCD_B4
PK4/LCD_B5
PK5/LCD_B6
PK6/LCD_B7
PK7/LCD_DE

N6	PJ0	LCD_R1
P6	PJ1	LCD_R2
T6	PJ2	LCD_R3
U6	PJ3	LCD_R4
U7	PJ4	LCD_R5
R12	PJ5	LCD_R6
N15	PJ6	LCD_R7
N14	PJ7	LCD_G0
N13	PJ8	LCD_G1
M14	PJ9	LCD_G2
L14	PJ10	LCD_G3
K14	PJ11	LCD_G4
D11	PJ12	LCD_B0
E10	PJ13	LCD_B1
D10	PJ14	LCD_B2
B10	PJ15	LCD_B3
J14	PK0	LCD_G5
J15	PK1	LCD_G6
H17	PK2	LCD_G7
C8	PK3	LCD_B4
B8	PK4	LCD_B5
A8	PK5	LCD_B6
C7	PK6	LCD_B7
D7	PK7	LCD_DE

STM32H750XBH6

Title:	
ATK_STM32H750_CORE_BOARD	
Author:	Size:
正点原子团队	A4
Date:	File:
2023/12/21	STM32H750XB_PIN_GHJK.SchDoc
Revision:	Version:
*	V1.2



